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99/892649**APPLICATION TRANSMITTAL LETTER (Large Entity)**

In re Application of: Siamack Nemazie et al.	Docket No.: 0924-MS-L-DIV1 RPB REF: CRUS-0219
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Serial Number: UNASSIGNED	Art Unit: UNASSIGNED
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Filing Date: June 27, 2001	Examiner: UNASSIGNED
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**Title: MIXED-SIGNAL SINGLE-CHIP INTEGRATED SYSTEM ELECTRONICS
FOR DATA STORAGE DEVICES**

TO THE ASSISTANT COMMISSIONER FOR PATENTS

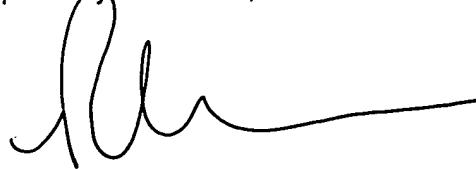
Transmitted herewith is a new U.S. Patent Application comprising **FIFTY-THREE (53)** Pages of Specification, including **FORTY-NINE (49)** Claims, along with **FIVE (5)** Sheets of Formal. Also enclosed are a Declaration & Power of Attorney and Information Disclosure Statement.

The filing fee is calculated as follows:

CLAIMS	TOTAL CLAIMS	BASIC CLAIMS	NUMBER EXTRA	PTO RATE	ADDITIONAL FEE DUE
TOTAL	49	20	29.00	18.00	522.00
INDEPENDENT	6	3	3.00	80.00	240.00
Multiple Dependent Claim Fee			0.00	270.00	0.00
				BASIC FILING FEE (LARGE ENTITY)	
					710.00
				TOTAL FEE DUE (CHECK ENCLOSED)	
					1,472.00

The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to Deposit Account No. 50-1393

Respectfully submitted,



Date: **June 27, 2001**

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